



AO4805

Dual P-Channel Enhancement Mode Field Effect Transistor

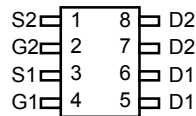


General Description

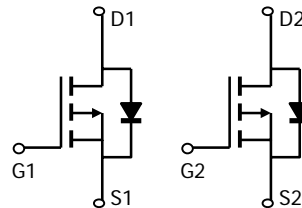
The AO4805 uses advanced trench technology to provide excellent $R_{DS(ON)}$, and ultra-low low gate charge with a 25V gate rating. This device is suitable for use as a load switch or in PWM applications. *AO4805 is Pb-free (meets ROHS & Sony 259 specifications)*

Features

- $V_{DS} (V) = -30V$
- $I_D = -8A (V_{GS} = -20V)$
- $R_{DS(ON)} < 18m\Omega (V_{GS} = -20V)$
- $R_{DS(ON)} < 19m\Omega (V_{GS} = -10V)$



SOIC-8



Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 25	V
Continuous Drain Current ^{AF}	$T_A=25^\circ C$ $T_A=70^\circ C$	-8	A
		I_D	
Pulsed Drain Current ^B	I_{DM}	-40	
Power Dissipation ^A	$T_A=25^\circ C$ $T_A=70^\circ C$	2	W
		P_D	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	$t \leq 10s$	50	$^\circ C/W$
Maximum Junction-to-Ambient ^A		Steady-State	73	
Maximum Junction-to-Lead ^C	$R_{\theta JL}$	Steady-State	31	$^\circ C/W$

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=-250\mu\text{A}, V_{GS}=0\text{V}$	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-30\text{V}, V_{GS}=0\text{V}$ $T_J=55^\circ\text{C}$			-1 -5	μA
I_{GSS}	Gate-Body leakage current	$V_{DS}=0\text{V}, V_{GS}=\pm 25\text{V}$			± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu\text{A}$	-1.7	-2.5	-3	V
$I_{D(ON)}$	On state drain current	$V_{GS}=-10\text{V}, V_{DS}=-5\text{V}$	-40			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10\text{V}, I_D=-8\text{A}$ $T_J=125^\circ\text{C}$		16 20.5	19 25	$\text{m}\Omega$
		$V_{GS}=-20\text{V}, I_D=-8\text{A}$		15	18	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}, I_D=-5\text{A}$		33		$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS}=-5\text{V}, I_D=-8\text{A}$	16	21		S
V_{SD}	Diode Forward Voltage	$I_S=-1\text{A}, V_{GS}=0\text{V}$		-0.75	-1	V
I_S	Maximum Body-Diode Continuous Current				-2.6	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=-15\text{V}, f=1\text{MHz}$		2076	2500	pF
C_{oss}	Output Capacitance		503			pF
C_{rSS}	Reverse Transfer Capacitance		302	423		pF
R_g	Gate resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	1	2	3	Ω
SWITCHING PARAMETERS						
Q_g	Total Gate Charge	$V_{GS}=-10\text{V}, V_{DS}=-15\text{V}, I_D=-8\text{A}$		39	45	nC
Q_{gs}	Gate Source Charge		8			nC
Q_{gd}	Gate Drain Charge		11.4			nC
$t_{D(on)}$	Turn-On Delay Time	$V_{GS}=-10\text{V}, V_{DS}=-15\text{V}, R_L=1.8\Omega,$ $R_{GEN}=3\Omega$		12.7		ns
t_r	Turn-On Rise Time		7			ns
$t_{D(off)}$	Turn-Off Delay Time		25.2			ns
t_f	Turn-Off Fall Time		12			ns
t_{rr}	Body Diode Reverse Recovery Time	$I_F=-8\text{A}, dI/dt=100\text{A}/\mu\text{s}$		32	40	ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=-8\text{A}, dI/dt=100\text{A}/\mu\text{s}$		26		nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

C: The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.

D: The static characteristics in Figures 1 to 6 are obtained using $<300\mu\text{s}$ pulses, duty cycle 0.5% max.

E: These tests are performed with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The SOA curve provides a single pulse rating.

F: The current rating is based on the $\leq 10\text{s}$ junction to ambient thermal resistance rating.

Rev5: Dec 2006

THIS PRODUCT HAS BEEN DESIGNED AND QUALIFIED FOR THE CONSUMER MARKET. APPLICATIONS OR USES AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS ARE NOT AUTHORIZED. AOS DOES NOT ASSUME ANY LIABILITY ARISING OUT OF SUCH APPLICATIONS OR USES OF ITS PRODUCTS. AOS RESERVES THE RIGHT TO IMPROVE PRODUCT DESIGN, FUNCTIONS AND RELIABILITY WITHOUT NOTICE.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

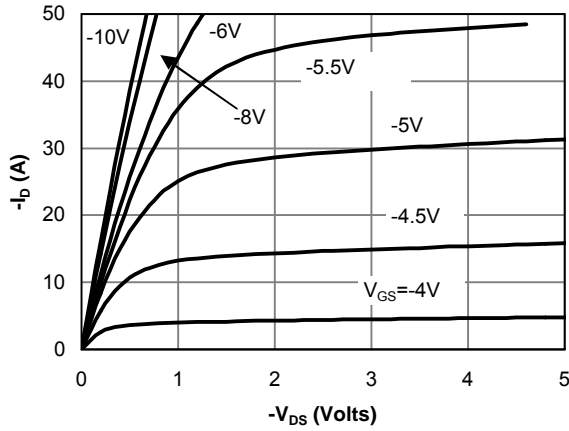


Fig 1: On-Region Characteristics

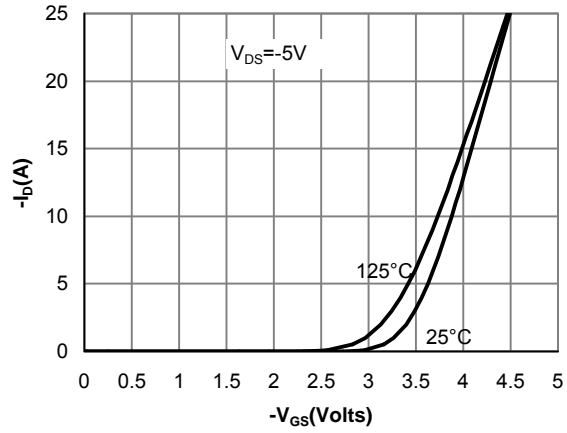


Figure 2: Transfer Characteristics

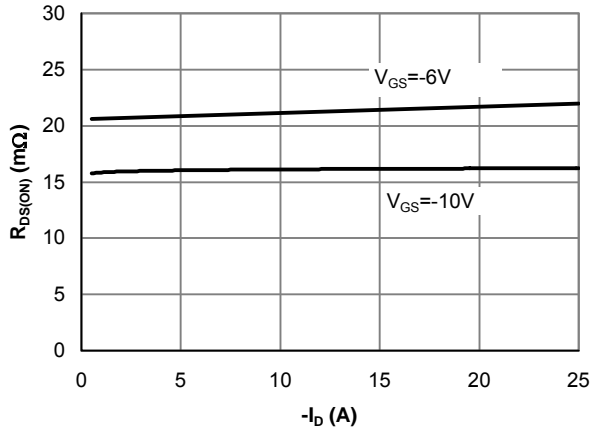


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

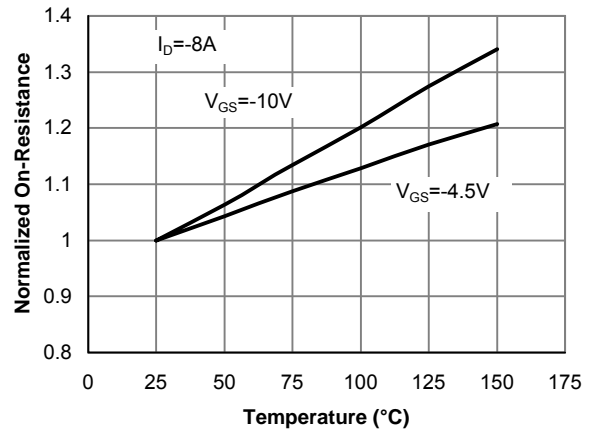


Figure 4: On-Resistance vs. Junction Temperature

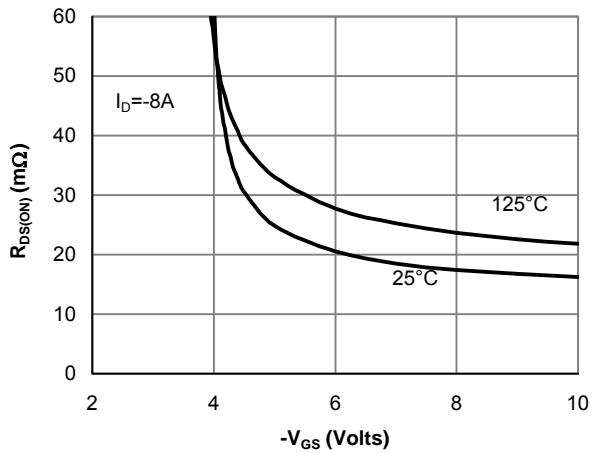


Figure 5: On-Resistance vs. Gate-Source Voltage

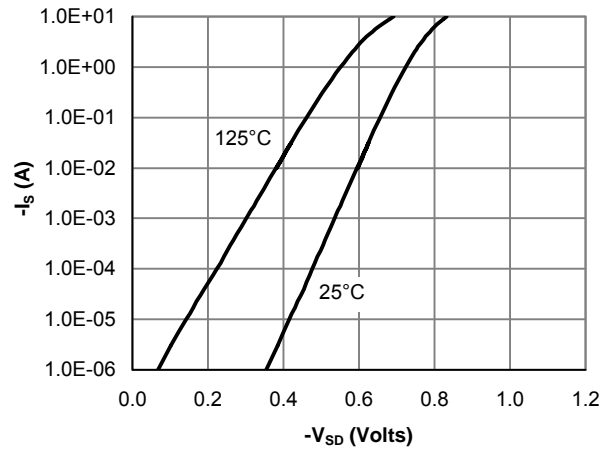


Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

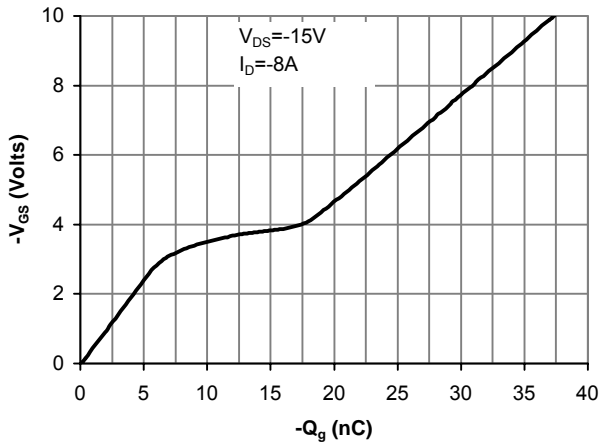


Figure 7: Gate-Charge Characteristics

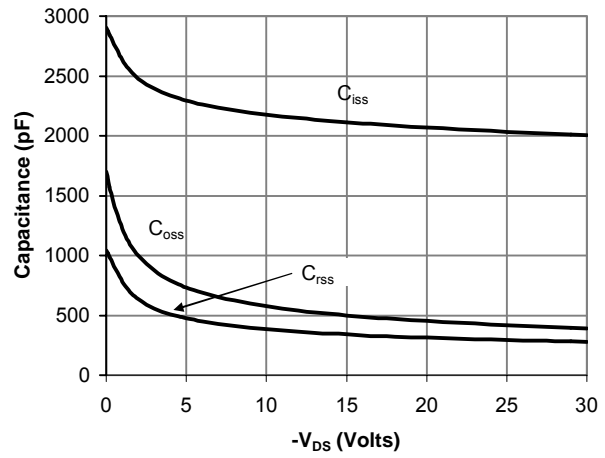


Figure 8: Capacitance Characteristics

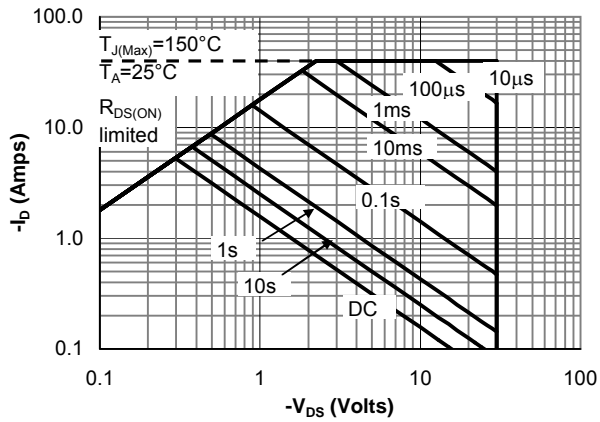


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

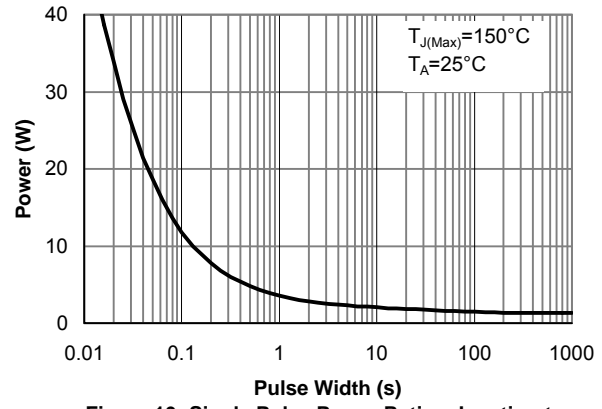


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

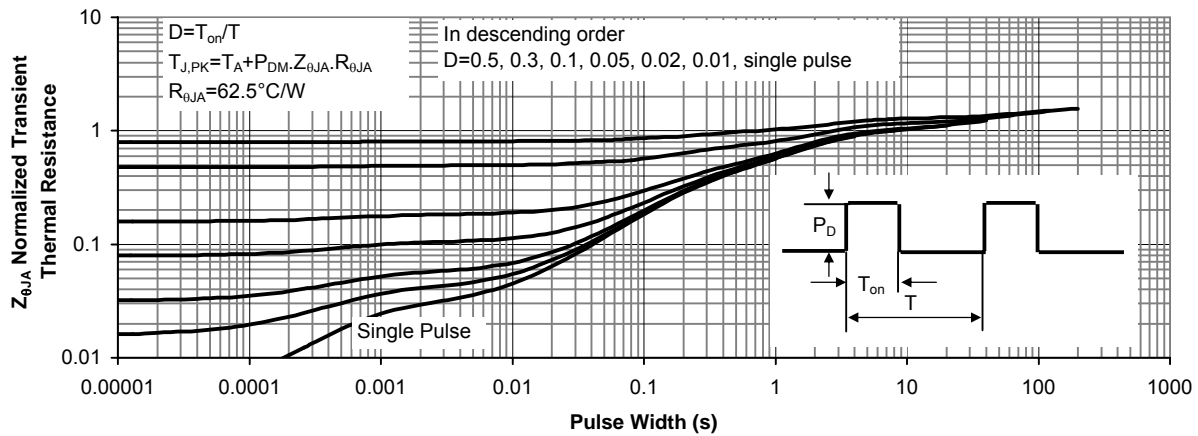


Figure 11: Normalized Maximum Transient Thermal Impedance